

PATENT
450106-02849REMARKS

The Office Action in the above-identified application has been carefully considered and this amendment has been presented to place this application in condition for allowance. Accordingly, reexamination and reconsideration of this application are respectfully requested.

Claims 1-2, 6-14, and 18-24 are in the present application. It is submitted that these claims are patentably distinct over the prior art cited by the Examiner, and that these claims are in full compliance with the requirements of 35 U.S.C. § 112. The changes to the claims, as presented herein, are not made for the purpose of patentability within the meaning of 35 U.S.C. sections 101, 102, 103 or 112. Rather, these changes are made simply for clarification and to round out the scope of protection to which Applicants are entitled. Claims 3-5 and 15-17 are canceled.

The disclosure was objected to because of various informalities. Applicants have amended each of the informalities as cited by the Examiner. Accordingly, Applicants believe this objection has been overcome.

Claims 1-4, 7-9, 13-16, and 19-21 were rejected under 35 U.S.C. § 102(b) as being anticipated by Chimoto et al. (U.S. Patent No. 5,838,383). Claims 5 and 17 were rejected under 35 U.S.C. § 103(a) as being unpatentable over Chimoto.

A feature of the present invention is to transfer a high layer command (and not on a real time basis) from the host processing block through the bus and also to transfer data streams without increasing the host processing block's CPU speed or the memory capacity. Accordingly,

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Applicants have amended claims 1 and 13 to include the limitations of canceled dependent claims 3-5 and 15-17, respectively. Claim 1 now recited "a host processing block for controlling said digital processing apparatus by outputting a command of a high layer and not on a real time basis." (Claim 1; Claim 13 contains similar limitations) Hence, the independent claims now include further limitations of the host processing block based on the description provided at page 10, lines 2-16; and high layer commands based on the descriptions provided at page 13, lines 14-20 and page 10, lines 2-10. The arithmetic operation limitation has been removed from these claims.

The Examiner contends Chimoto discloses "the CPU 313 supplies prescribed parameters through the DMA device 312 and the bus 302 to the digital broadcast-signal receiving module 304." (Chimoto 9:27-34) Applicants assert that Chimoto's parameters are more analogous to low level commands than the high level commands disclosed in the present invention. For at least this reason, Applicants believe Chimoto fails to disclose the host processing block outputting a high layer command on other than a real time basis. Accordingly, the rejected claims should now be allowed.

Claims 6 and 18 were rejected under 35 U.S.C. § 103(a) as being unpatentable over Chimoto in view of Humpleman et al. (U.S. Patent 6,198,479). Claims 10-12 and 22-24 were rejected under 35 U.S.C. § 103(a) as being unpatentable over Chimoto in view of Trovato et al. (U.S. Patent 6,469,742). Humpleman and Trovato are relied upon solely to meet various limitations of the dependent claims. However, Humpleman and Trovato fail to meet the same limitations of the independent claims as discussed above in relation to Chimoto. For at least this

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reason, the combinations of Chimoto and Humpleman or Trovato fail to obviate the present invention and the rejected claims should now be allowed.

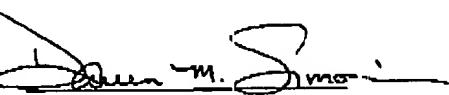
In view of the foregoing amendment and remarks, it is respectfully submitted that the application as now presented is in condition for allowance. Early and favorable reconsideration of the application are respectfully requested.

No additional fees are deemed to be required for the filing of this amendment, but if such are, the Examiner is hereby authorized to charge any insufficient fees or credit any overpayment associated with the above-identified application to Deposit Account No. 50-0320.

If any issues remain, or if the Examiner has any further suggestions, he/she is invited to call the undersigned at the telephone number provided below. The Examiner's consideration of this matter is gratefully acknowledged.

Respectfully submitted,
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